

Name: _____

Honor Pledge: I am adhering to the Honor Code while taking this test.

Signature: _____

Date: _____

25 points total.

1. The trustees of Karnaugh University (Alice, Bob, Carol, and David) have convened once more! They're considering a proposal to require all students to take Digital Electronics. If Alice, Bob, and Carol all vote YES, the proposal will pass. If Bob, Carol, and David all vote NO, the proposal will fail. In all other cases, the University will make its decision without even caring how the trustees voted.

A. (One point.) Complete this truth table, where 1 as an input represents a YES vote, and 1 as an output represents the passage of the proposal. X as an output represents "don't care."

A	B	C	D	PASS
0	0	0	0	0
0	0	0	1	X
0	0	1	0	X
0	0	1	1	X
0	1	0	0	X
0	1	0	1	X
0	1	1	0	X
0	1	1	1	X
1	0	0	0	0
1	0	0	1	X
1	0	1	0	X
1	0	1	1	X
1	1	0	0	X
1	1	0	1	X
1	1	1	0	1
1	1	1	1	1

B. (One point.) Write out a K map equivalent to the truth table.

		CD			
		00	01	11	10
AB	00	0	X	X	X
	01	X	X	X	X
	11	X	X	1	1
	10	0	X	X	X

C. (One point.) Write out any logic equation that implements the truth table. You may use as many (or as few) gates as you like.

There's an enormous number of possible choices because there are so many X's. The easiest choices are $PASS = B$ and $PASS = C$. (No gates required in either of these cases.)

D. (Three points.) Write a Verilog module (including the **module** and **endmodule** statements) that implements the circuit equivalent to your logic equation.

```
module partD (input B, output PASS);
```

```
    assign PASS = B;
```

```
endmodule
```

2. The surviving victors (Beetee, Peeta, Annie, Enobaria, Katniss, Haymitch, and Johanna) are voting on whether to hold the Hunger Games one last time. Peeta votes NO. Haymitch votes the same way as Katniss, and Johanna votes the opposite way as Annie. The majority of the seven votes determines the outcome.

A. (One point.) Complete this truth table, where 1 as an input represents a YES vote, and 1 as an output indicates that a majority of the seven victors voted YES.

Beetee	Annie	Enobaria	Katniss	Majority
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	1

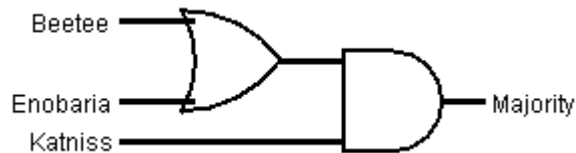
B. (One point.) Write out a K map equivalent to the truth table.

		EnobariaKatniss			
		00	01	11	10
BeeteeAnnie	00	0	0	1	0
	01	0	0	1	0
	11	0	1	1	0
	10	0	1	1	0

C. (One point.) Write out minimized logic equation (using as few gates as possible) equivalent to the K map. (Hint: Only two gates are required.)

Majority = Katniss & (Beetee | Enobaria)

D. (One point.) Draw the circuit diagram equivalent to your response to part C.



E. (One point.) We know that the sum of the seven votes is $\text{Sum} = (\text{Beetee} + \text{Peeta} + \text{Annie} + \text{Enobaria} + \text{Katniss} + \text{Haymitch} + \text{Johanna})$. Now write an expression for Sum in terms of only three of the input variables.

Peeta = 0, Haymitch = Katniss, and $(\text{Annie} + \text{Johanna}) = 1$. Therefore:

$\text{Sum} = 1 + \text{Beetee} + \text{Enobaria} + 2 * \text{Katniss}$

F. (Seven points.) Using your result from part E, write a Verilog module that computes a sum and uses an **if** statement (but no explicit Boolean operators) to determine whether the majority of victors voted YES or NO.

```
module partF (input Beetee, input Enobaria, input Katniss, output reg Majority);  
    wire [2:0] sum;  
    assign sum = 1 + Beetee + Enobaria + 2*Katniss;  
    always @ (*)  
    if (sum < 4) Majority = 0;  
    else Majority = 1;  
endmodule
```

3. (One point.) Convert **11101101** (assuming two's complement representation) into base 10.

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4. (Six points.) Compute the binary sums, indicating the values of the carry and overflow flags.

00111110
+10111110
11111100 Carry: 0 Overflow: 0

01000101
+11010001
00010110 Carry: 1 Overflow: 0